



A Self-Optimising Simulator For A Coarse-Grained Reconfigurable Array

Jack Whitham and Neil Audsley

jack@cs.york.ac.uk

Real Time Systems Group

Department of Computer Science

University of York





Overview

- MCGREP project summary
- Simulator wanted!
- Other simulators
- Design & Implementation
- Evaluation
- Applications
- Conclusion



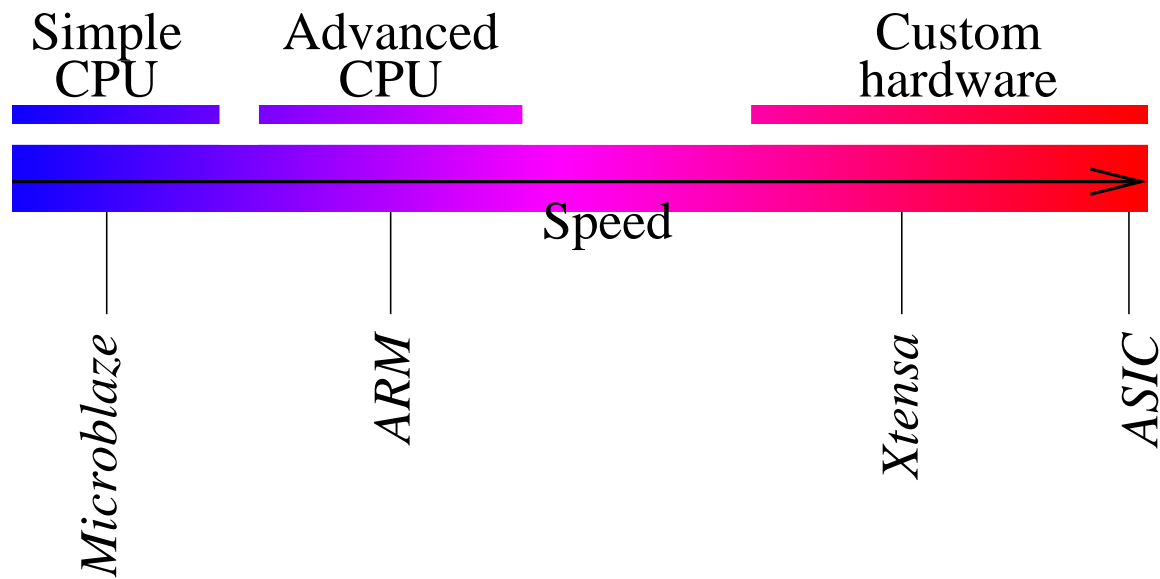
Real-Time Embedded Systems

Special challenges for Real-time Embedded (RTE) systems:

1. High Performance.
2. Easy WCET Analysis.
3. General Purpose.

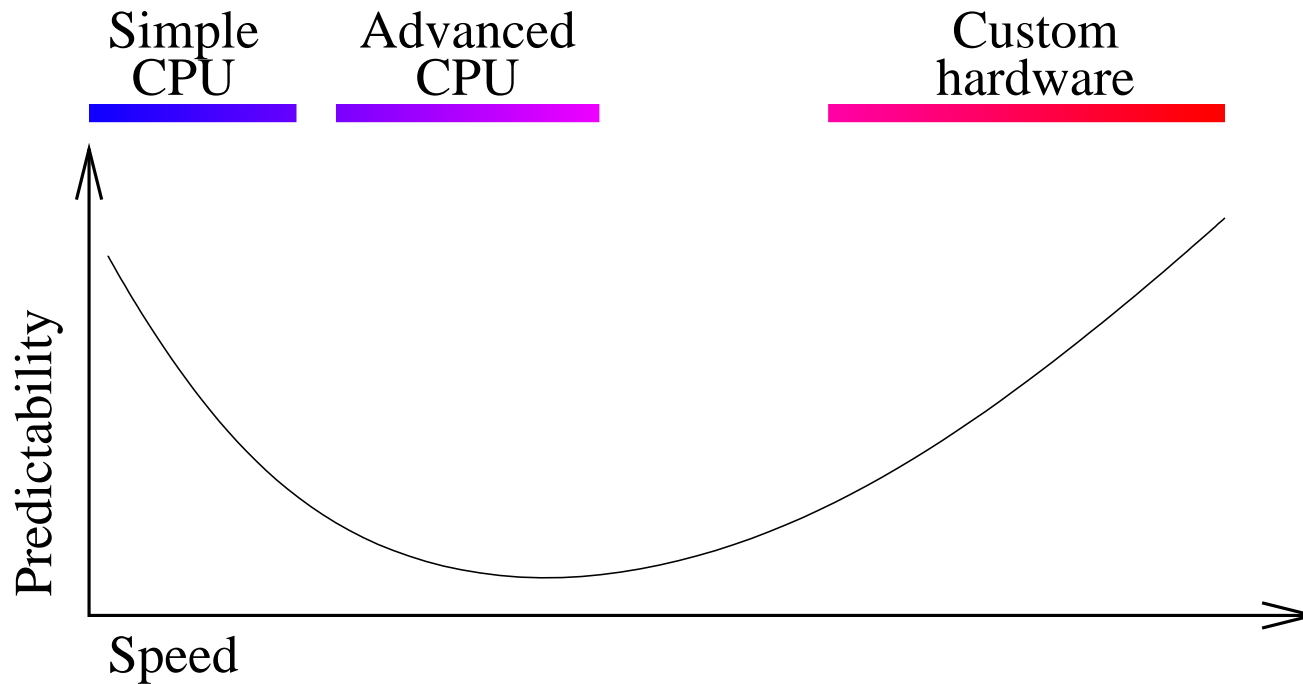
RTE Requirements

(1): High Performance



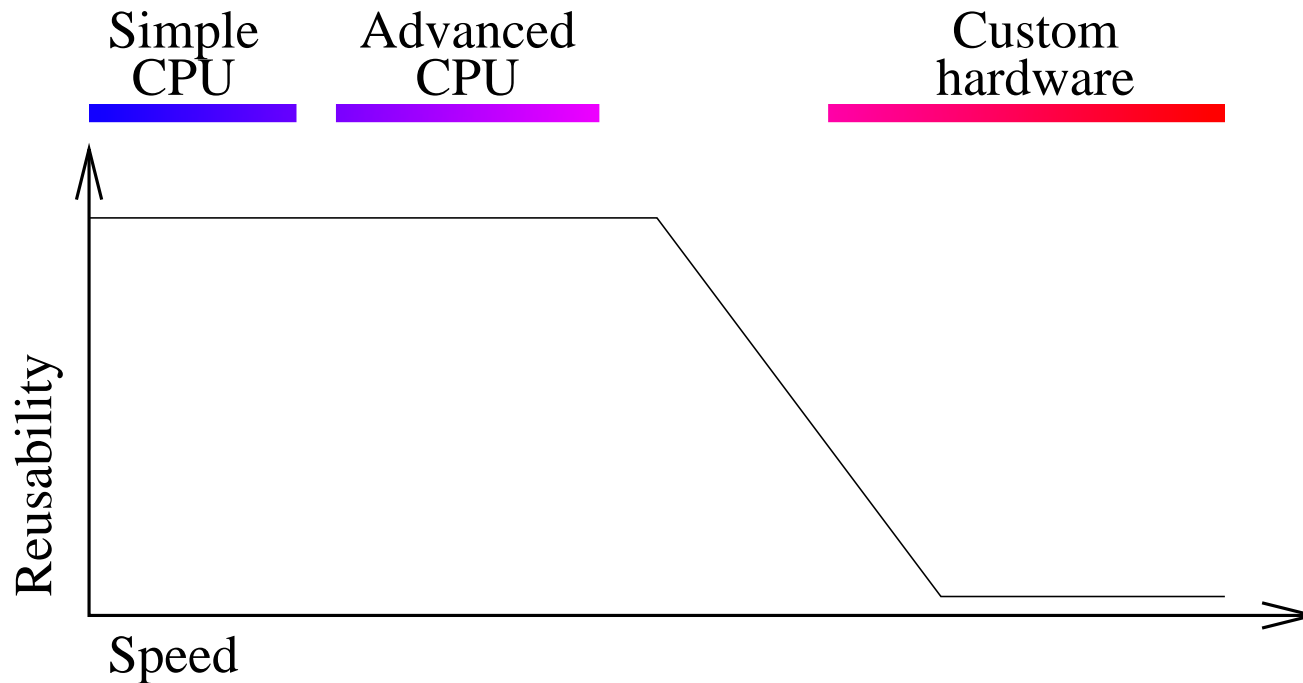
RTE Requirements

(2): Easy WCET Analysis



RTE Requirements

(3): General Purpose





General Purpose?

But embedded systems are fixed purpose!

- Or are they?



General Purpose?

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- Or are they?

General purpose features also useful for:

- Bug fixes and extensions,
- Reuse of old designs,
- Run-time adaptation.



MCGREP

Microcoded coarse-grained reconfigurable processor: software controlled CPU



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1. **Speed and predictability**, approaching **custom hardware**.

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Aims of MCGREP:

1. **Speed and predictability**, approaching **custom hardware**.
2. General purpose **reusability**, like a **simple CPU**.





Architecture

- Both **Coarse-grained Array** and **CPU**.



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- Many reprogrammable processors.



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- Many reprogrammable processors.
- Each can run programs from external memory and from **internal microcode**.
- Microcode is used for **hotspot** execution.



MCGREP Paradigm

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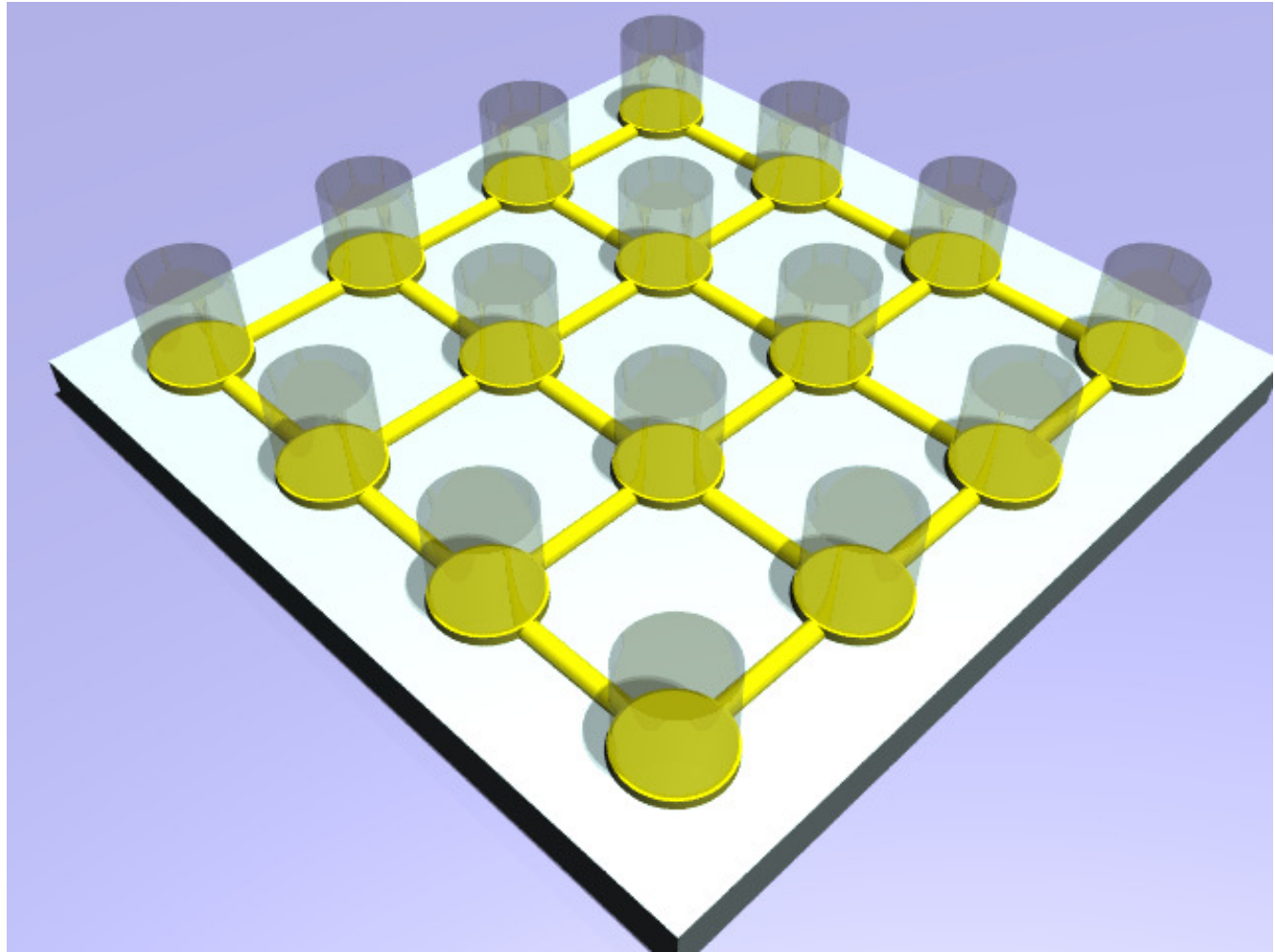


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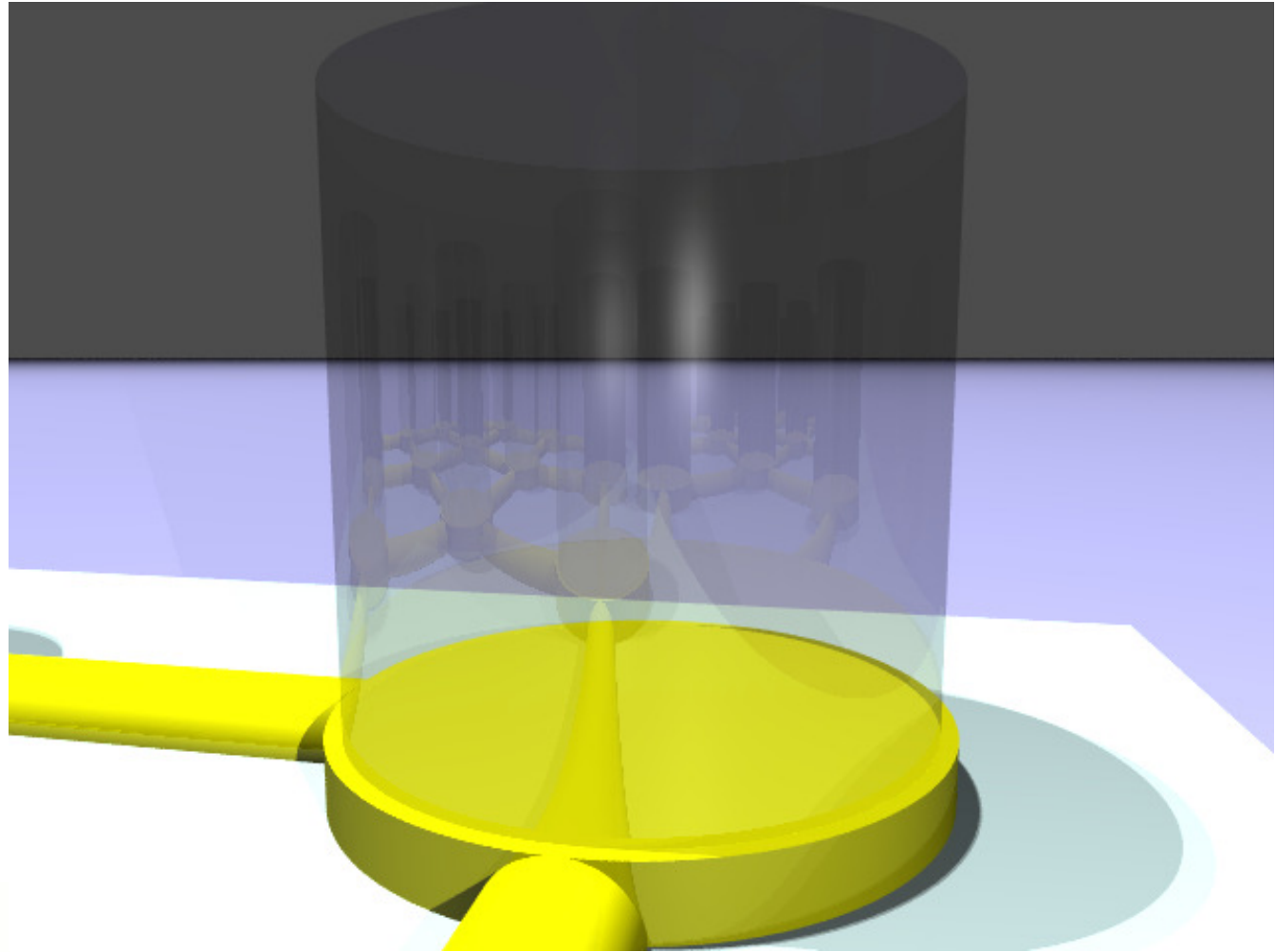
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- Applications are speeded up, predictability and reusability are retained.



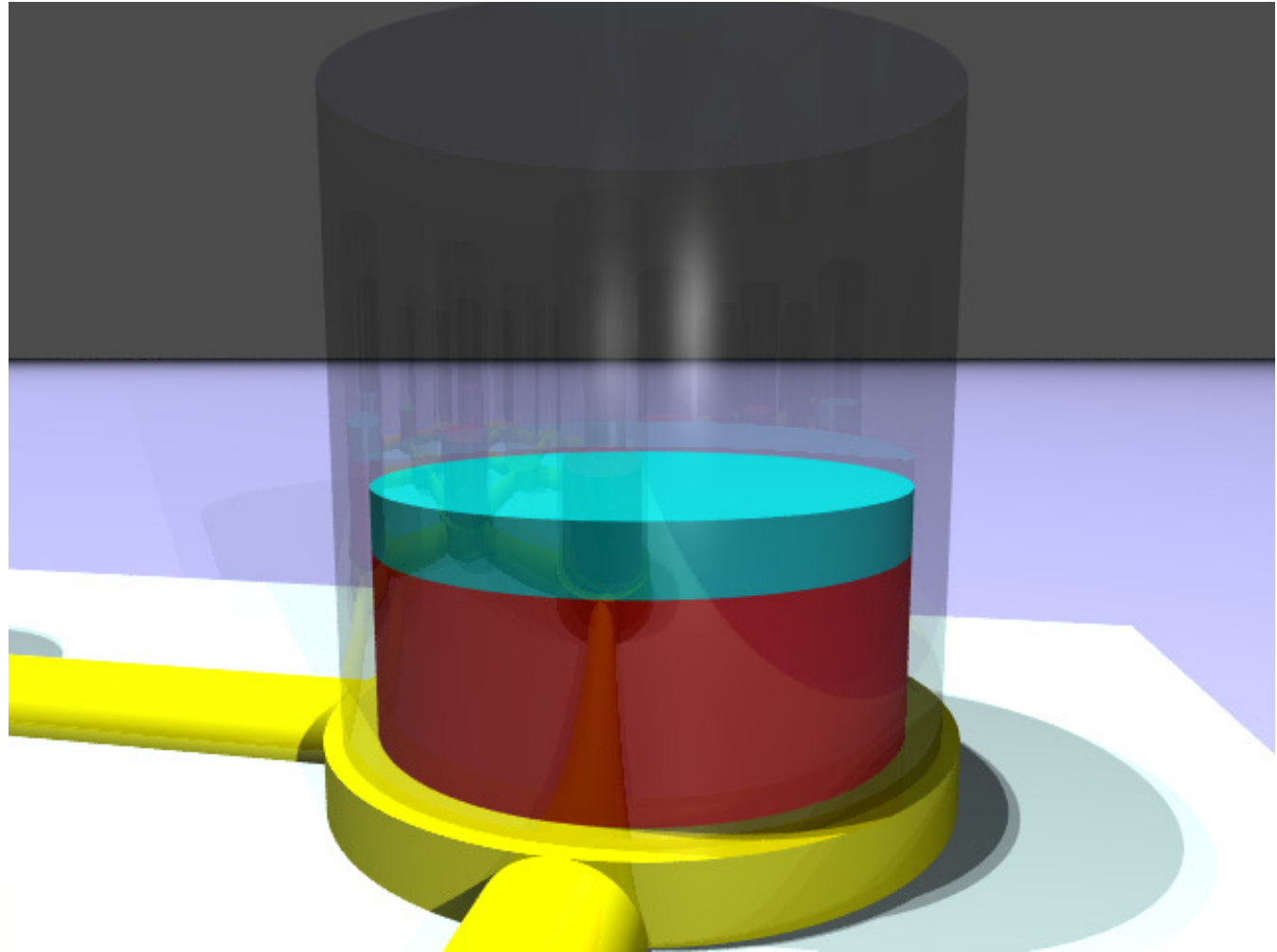
Distributing Hotspots (1)



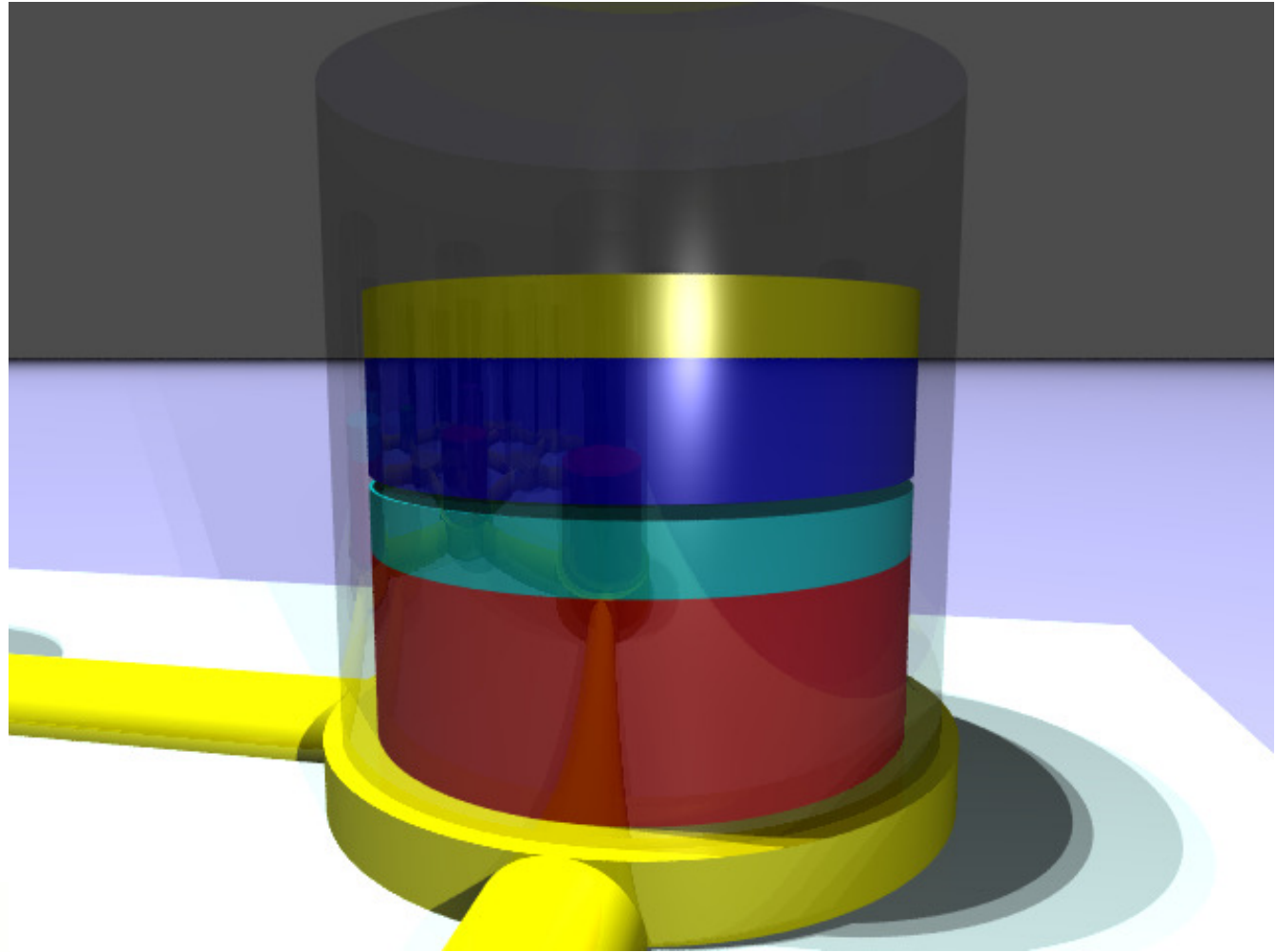
Distributing Hotspots (2)



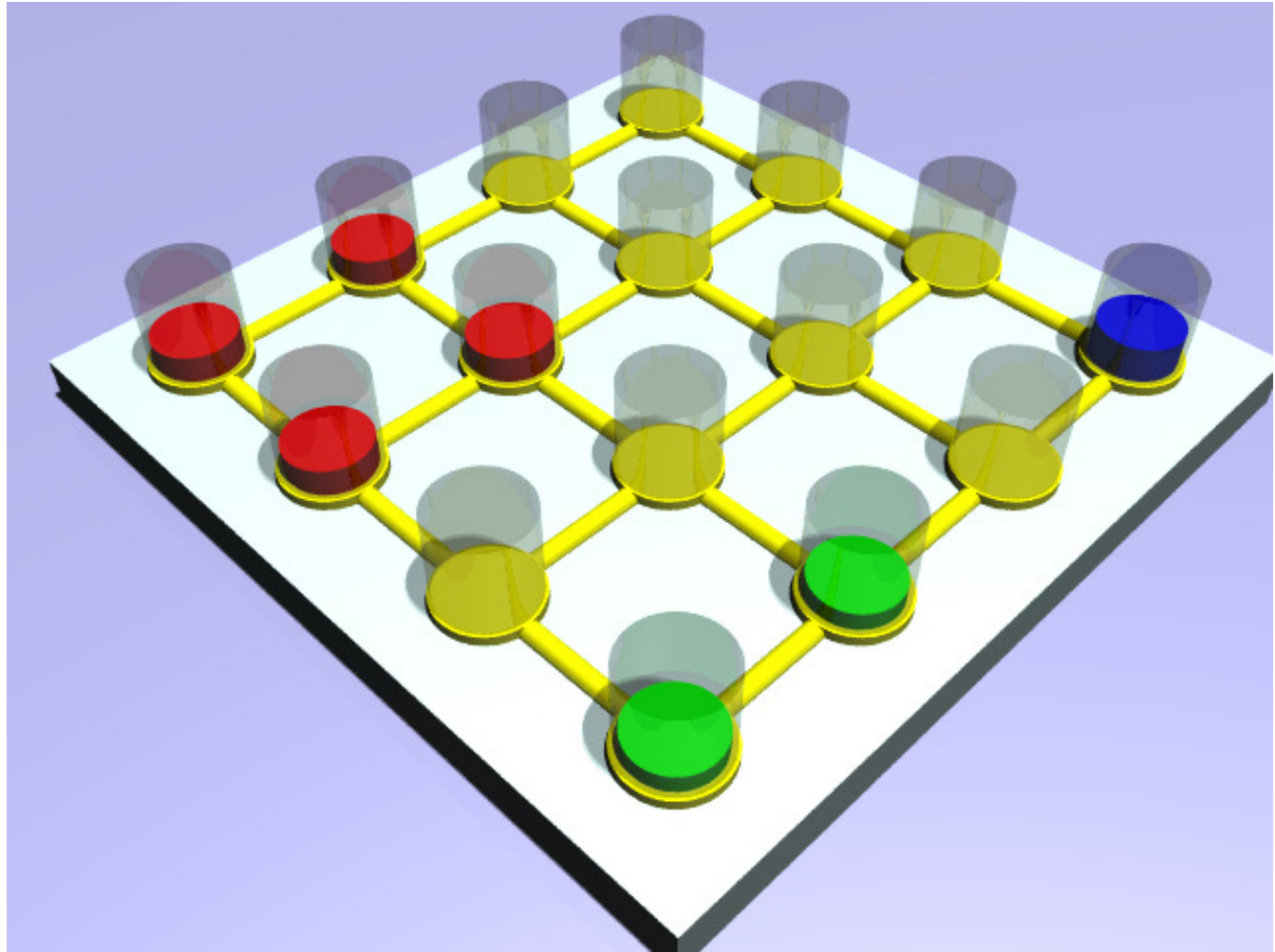
Distributing Hotspots (3)



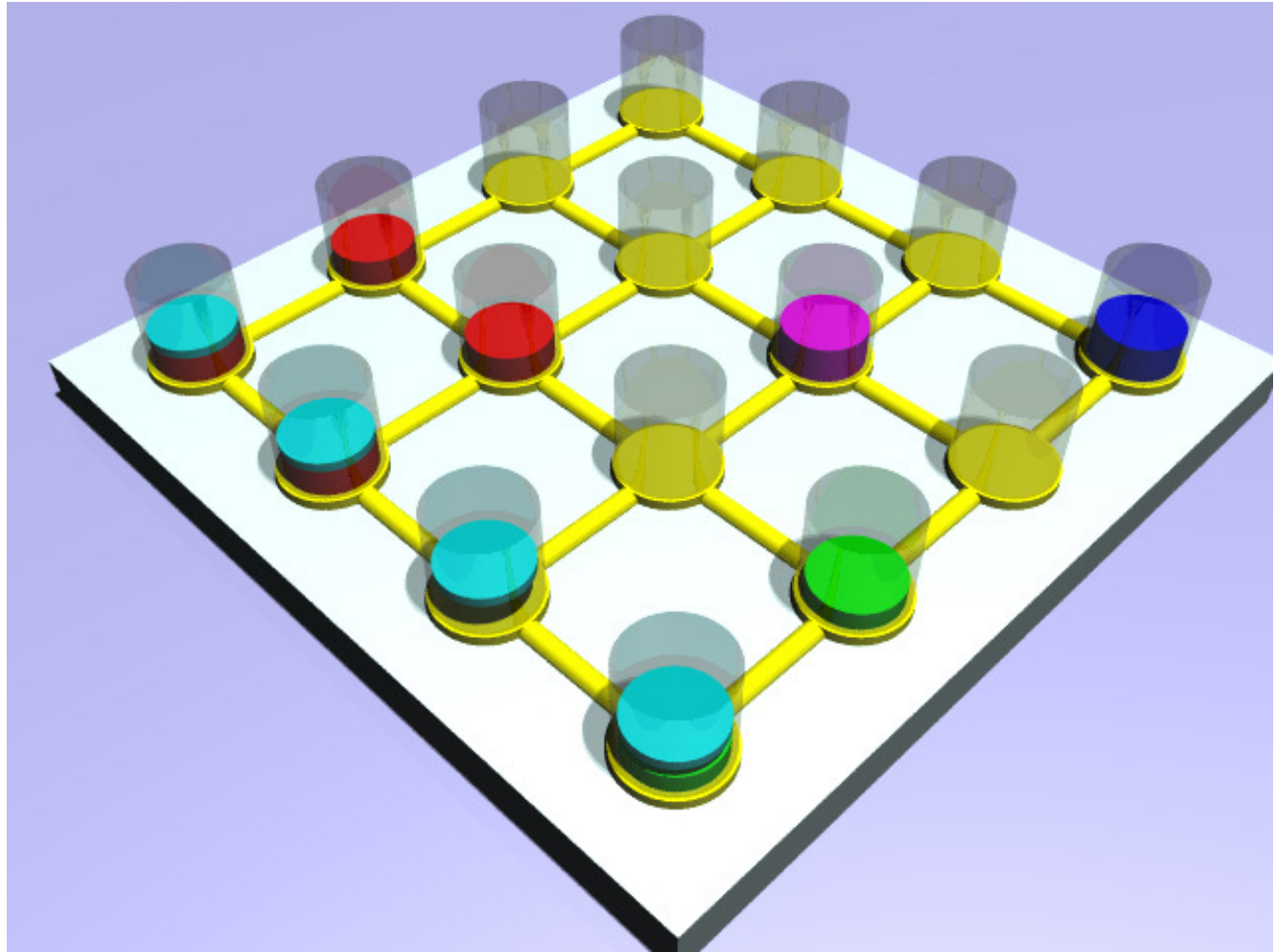
Distributing Hotspots (4)



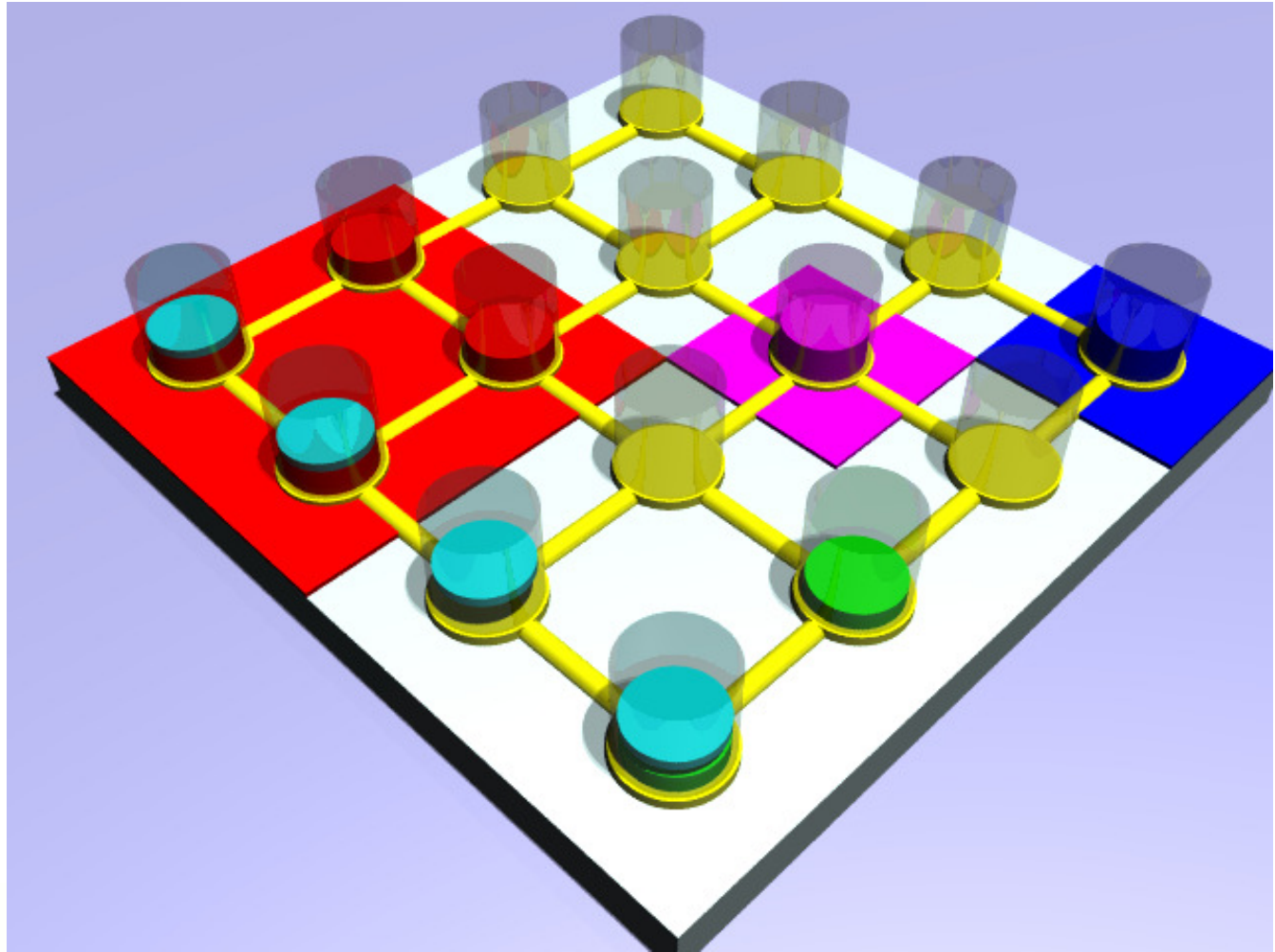
Distributing Hotspots (5)



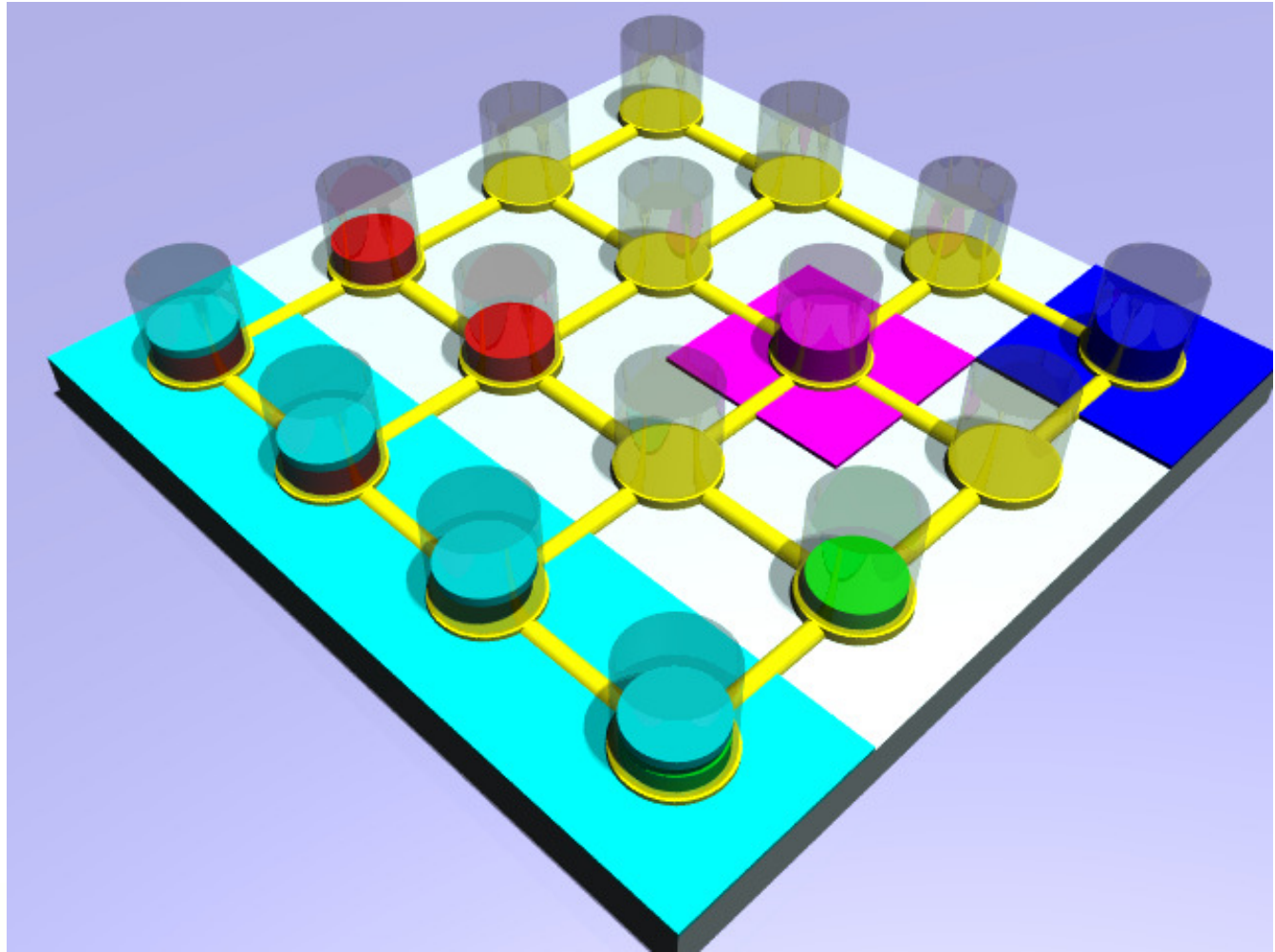
Distributing Hotspots (6)



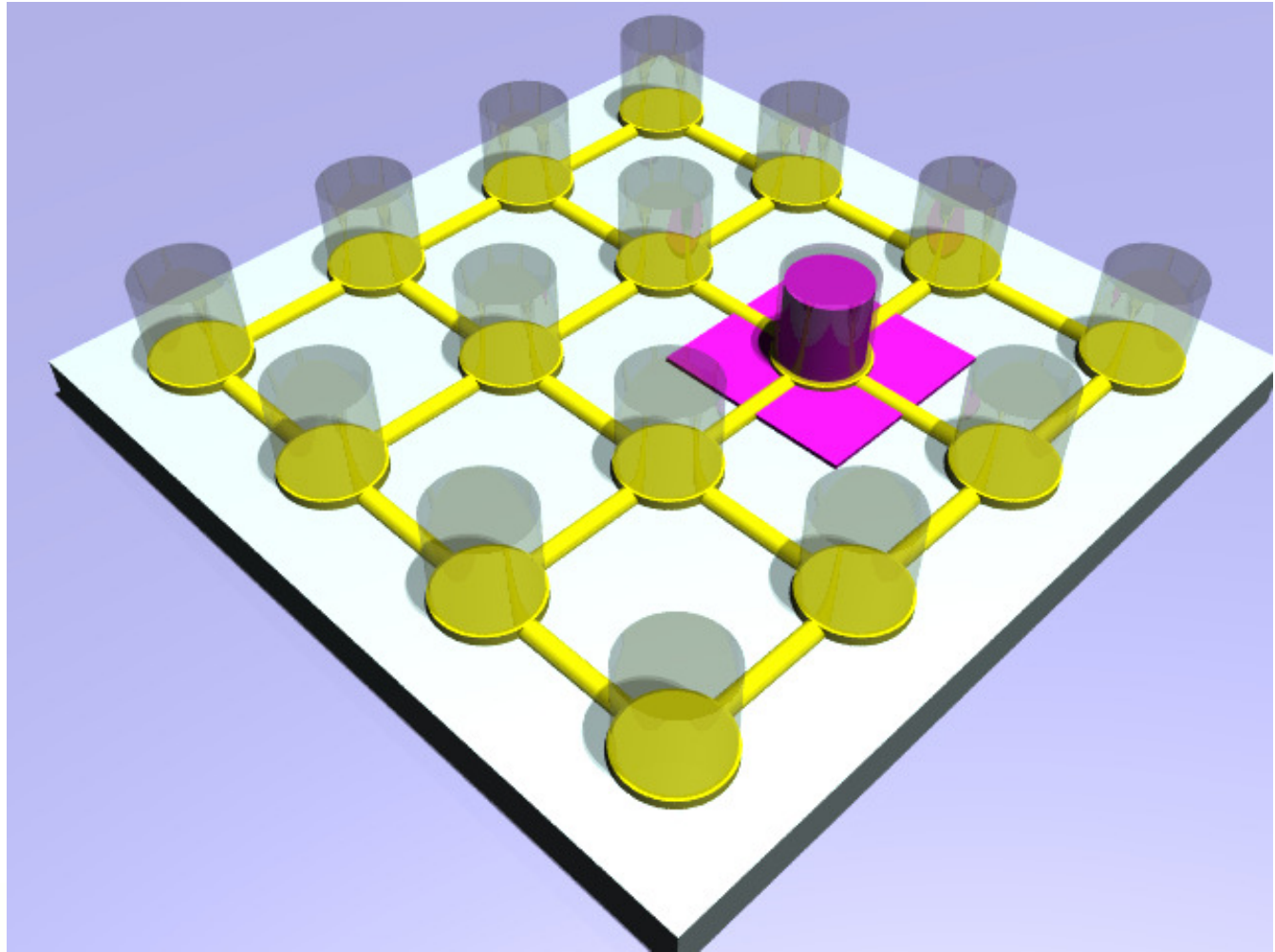
Distributing Hotspots (7)



Distributing Hotspots (8)



Distributing Hotspots (9)





Simulation

Simulation is

- The process of modelling the behaviour of one system using another.



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Simulation allows experimentation with a new design while only parts of the design are complete.



MCGREP Needs A Simulator

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3. for **debugging** MCGREP tools.



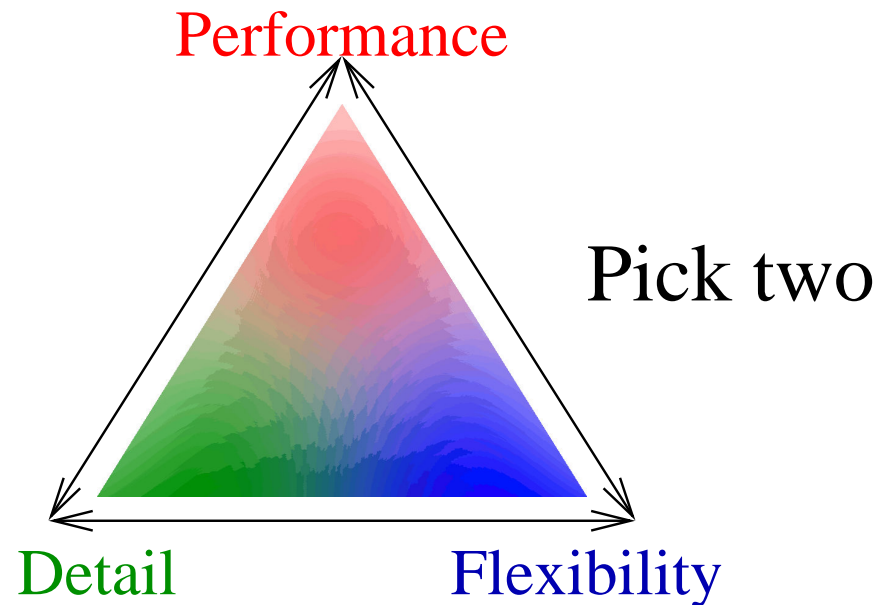
Two Classes of Simulator

There are two types of architectural simulator:

1. Functional (captures function but not timing),
2. Performance (captures function and timing).

“Tao of Simulation”

The tradeoffs in simulator design:



Example

SimpleScalar:

Name	Speed	Detail	Description
<code>sim-fast</code>	Most	Least	Functional, no checks
<code>sim-safe</code>			Functional, with checks
<code>sim-uop</code>			Functional, μ op
<code>sim-outorder</code>	Least	Most	Performance



MCGREP Simulator

Requirements are sorted into priority order:

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3. **Performance** - Speed is important for testing real applications.

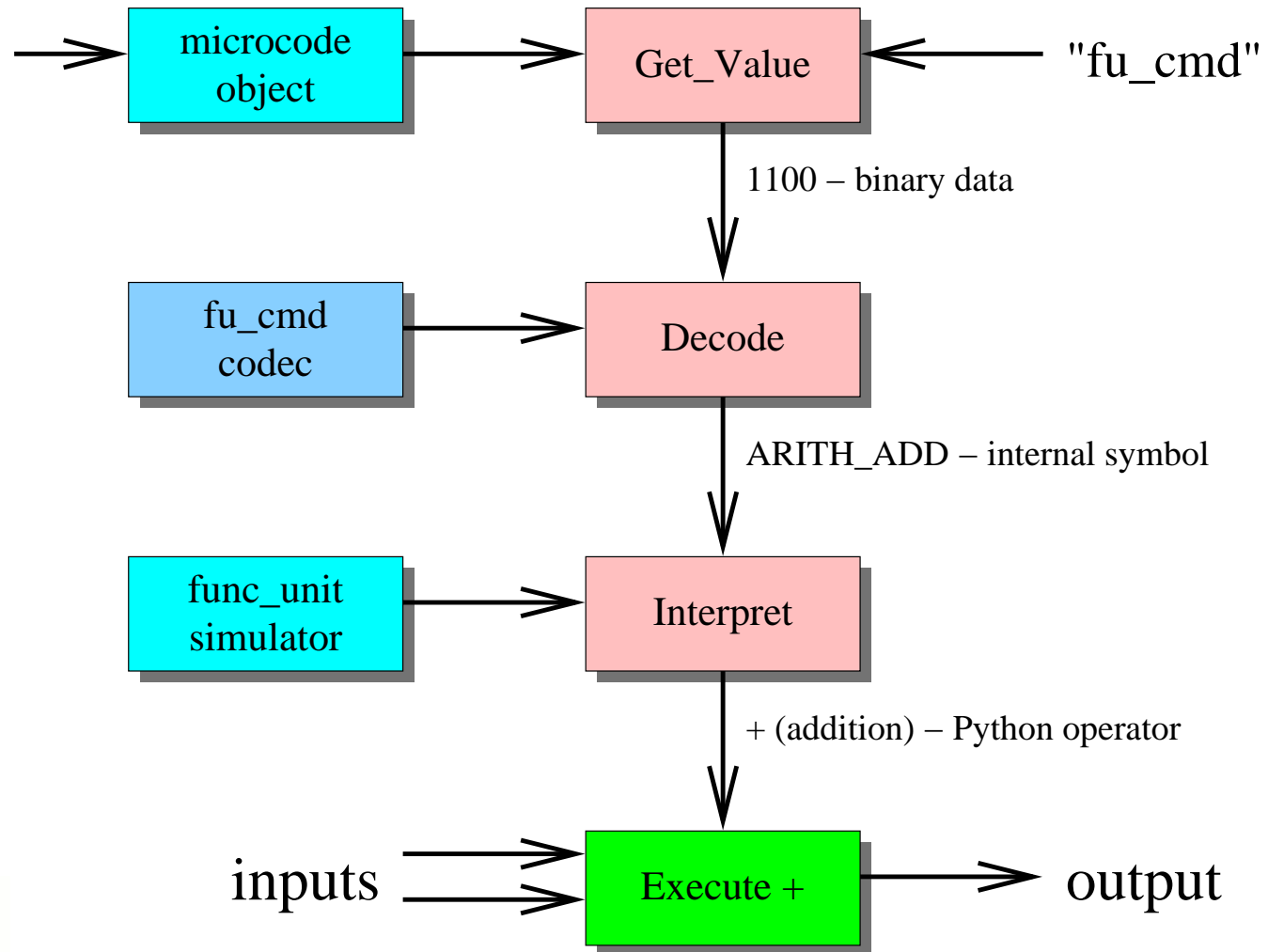


Simulator 1

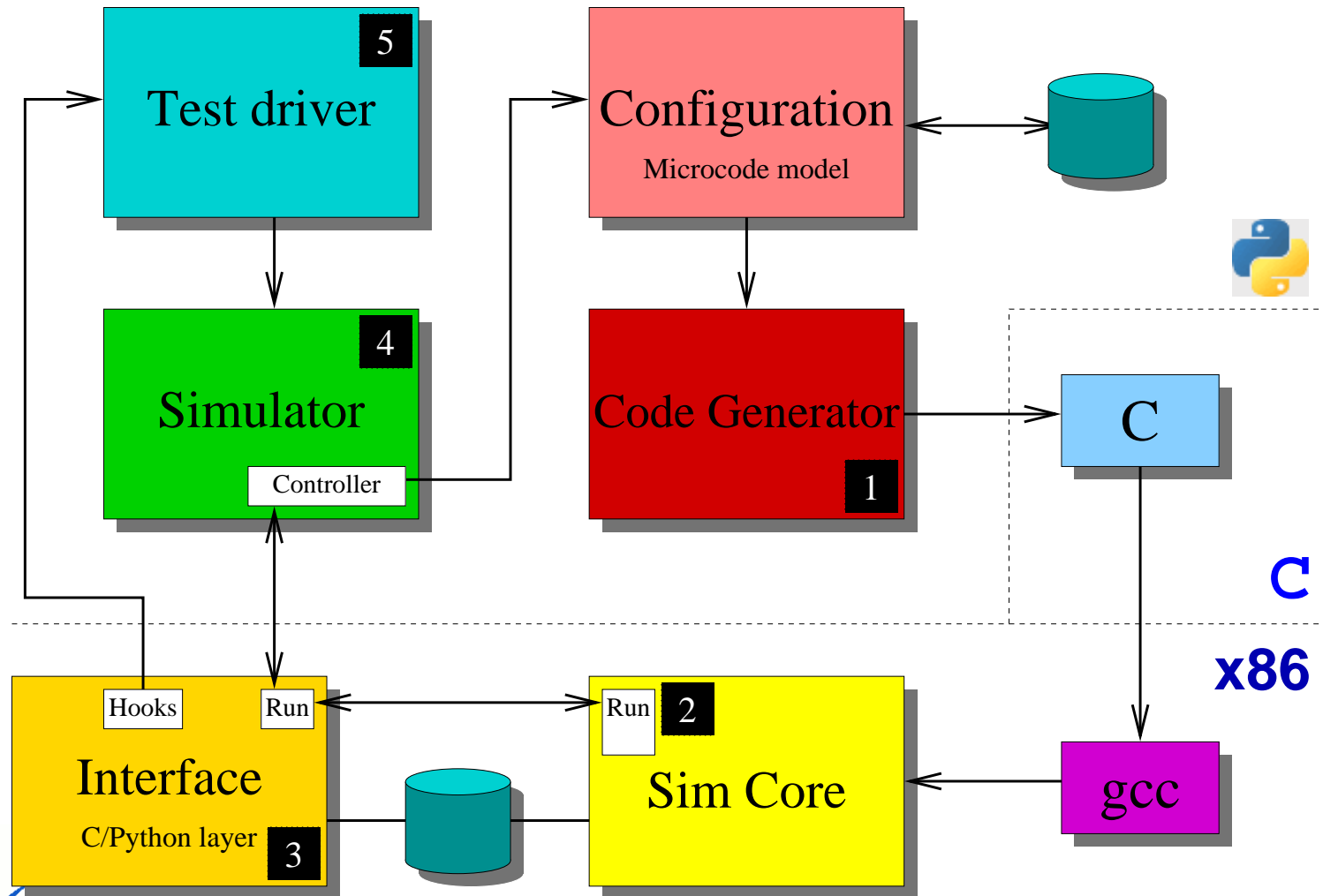
First simulator:

1. Written in Python,
2. Approx. 1000 instructions per second,
3. Detailed and flexible, but very poor performance,
4. Impossible to test real applications!

Poor Performance



New Simulator





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Flexibility

- First simulator was extended by subclassing.
- New simulator is extended by **hook functions**.
- This is sufficient for current experiments and tests.

Performance

Pentium 4 2.8GHz

5,010,000

SPARC 1.0GHz

2,540,000

Opteron 2.2GHz

6,920,000

Python Only

1240

(Figures in instructions per second)





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- Functional equivalence demonstrated!



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The simulator has been used for:

- Experiments on MCGREP.



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- And hardware development.



Conclusion

The simulator has enabled:

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- Unit testing of MCGREP hardware.
- Debugging.

It has also improved performance by a factor of 1000.



Questions?

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